Objectives
Define a methodology to modeling embedded real-time systems from the expression of requirements and by guarantying their traceability throughout realization process.

Projects plans & deliverables:
The project establishes the software development process on an methodology of modeling and management of the requirements follow-up by models refinements and supporting by tools. It supports the interoperability between modeling heterogeneous formalisms by the definition of semantic pivots.
A major objective will consist of the behaviors analysis in the software architecture by the use and the improvement of UML standard, and its MARTE and SysML profiles.

Major phases of the projects :

Status / achievements:
- State of the art document of tools and projects on MDE
- Case study bottom up engineering
- Formal description of industrial needs for seamless methodology guideline
- ATESSST result integration (EAST ADL intermediate profile)
- Two web sites (public and collaborative) www.memvatex.org

Coordinator: Siemens VDO

Business model

Partner: Cea List, Inria, Monditech, Utc

Durations: 3 years

Global budget: 2,147 M€

Funding: 0,98 M€ from ANR/RNTL

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